

**NOTICE OF APPEAL FROM THE EXAMINER  
TO THE BOARD OF PATENT APPEALS AND INTERFERENCES**

TI-33028

In re application of:

Brian Joseph

Serial No.: 09/532,510

For: Storage Efficient Minimization Logic

Filed: 03/21/2000

Group Art Unit: 2171

Mail Stop AF  
Commissioner for Patents  
P. O. Box 1450  
Alexandria, VA 22313-1450

Sir:

Applicants hereby appeal to the Board of Patent Appeals and Interferences from the decision in the Office

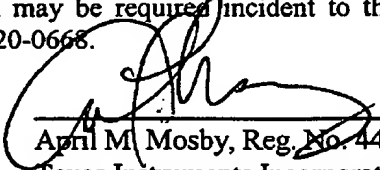
Action dated 04/23/2004 of the Primary Examiner finally rejecting claims 1,3,4,6-12,14-16, & 18-20.

The item (s) checked below are appropriate:

1. ☐ An extension of time to respond to the final rejection  
☐ was granted on  
☐ is requested for \_\_\_\_\_ month(s).
2. ☐ A timely response to the final rejection has been filed, as provided in 841 O.G. 1411.
3. ☒ Fee \$330.00:  
☐ Not required (*Fee paid in prior appeal*)  
☒ The Commissioner is hereby requested to grant any time extension that may be due, and is authorized to charge any fees which may be required incident to this filing, or credit any overpayment to Deposit Account No. 20-0668.

Signature [Rule 191(b)]

Post Office Address  
(to which correspondence is to be sent)

  
April M. Mosby, Reg. No. 44,955  
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Dallas, TX 75265

<p style="text-align: center;"><b>CERTIFICATE OF MAILING OR TRANSMISSION</b></p> <p>I hereby certify that the above correspondence is being deposited with the U.S. Postal Service with sufficient postage for First Class Mail in an envelope addressed to: Commissioner for Patents, Alexandria, VA 22313-1450 or is being facsimile transmitted to the USPTO on the date indicated below:</p> <p style="text-align: center;"><u>7-23-04</u></p> <p style="text-align: center;"><u>Jackie McBride</u></p> <p style="text-align: center;">Jackie McBride</p>
---------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------------

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From: April M. Mosby  
Texas Instruments Incorporated  
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## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of:  
Brian Joseph

Docket Number: TI-33028

Serial No.: 09/532,510

Art Unit: 2171

Filed: 03/21/2000

Examiner: Le, Uyen T

For: STORAGE EFFICIENT MINIMIZATION LOGIC

## CERTIFICATION OF FACSIMILE TRANSMISSION

I hereby certify that the following papers are being transmitted by facsimile to the  
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Jackie McBride  
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<input type="checkbox"/> <b>CONTINUATION APP'N</b> (# Pages)	
<input type="checkbox"/> <b>DIVISIONAL APP'N</b>	
<b>NAME OF INVENTOR(S):</b> <b>Brian Joseph</b>	
<b>TITLE OF INVENTION:</b> <b>Storage Efficient Minimization Logic</b>	
<b>TI FILE NO.:</b> <b>TI-33028</b>	<b>DEPOSIT ACCT. NO.:</b> <b>20-0668</b>
<b>FAXED: 07/23/2004</b> <b>DUE: 07/23/2004</b>	
<b>ATTY/SECY: AMM/jsm</b>	
<b>RECEIPT DATE &amp; SERIAL NO.:</b> <b>Serial No.: 09/532,510</b>	
<b>Filing Date: 03/21/2000</b>	

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